



Document Name M10 Version Notes	System M10	Developed for General Distribution
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10/24/2007 FPGA 8.1.10 PIC 3.1B

Problem	Description
Initial release.	

12/19/2007 FPGA 8.1.11 PIC 3.1D

Problem	Description
Weak pull-ups should be removed from port B.	Weak pull ups have been removed.
The pulse train needs to be faster.	The pulse train period and high time are now specified in units of 12.5ns. The high time is computed as $HT * 12.5$, and the period as $(PERIOD + 1) * 12.5$.
The MULL register was not being properly protected during the high priority interrupt.	Fixed. This probably had no effect on the operation of prior versions.

1/3/2008 FPGA 8.1.11 PIC 3.1E

Problem	Description
The "beacon message" bug needs to be applied to this version.	The fix has been applied to prevent unpredictable results that might be caused by the bug.

1/7/2008 FPGA 8.1.11 PIC 3.1F

Problem	Description
The communication lock-up fix reported for the I200 needs to be applied.	This has been traced to a communications state-machine problem which caused the device's communications to lock up under some circumstances. This problem has been fixed by clearing the interrupt flag immediately after a message is read out of the FPGA buffer by the PIC.

1/7/2008 FPGA 8.1.11 PIC 3.1F

Problem	Description
The communication lock-up fix reported for the I200 needs to be applied.	This has been traced to a communications state-machine problem which caused the device's communications to lock up under some circumstances. This problem has been fixed by clearing the interrupt flag immediately after a message is read out of the FPGA buffer by the PIC.

5/9/2008 FPGA 8.1.11 PIC 3.1G

Problem	Description
Support needs to be added for the M10C gating.	This has been added.