



Document Name I3200 Version Notes	System I3200	Developed for General Distribution
---	-----------------	---------------------------------------

1/09/2008 FPGA 5.3.5 PIC 3.1M

Problem	Description
Initial release.	

1/18/2008 FPGA 5.3.5 PIC 3.1O

Problem	Description
I/O messages are sometimes lost by the A500 poll.	This problem was traced to the Initiate() function which shut off low priority interrupts while setting the FPGA registers. This was done due to a previous problem with interrupt register saving (now fixed) and is no longer necessary. Because interrupts were turned off, incoming messages were not properly serviced by the PSI engine, causing a timeout. Interrupts are no longer turned off.

3/25/2008 FPGA 5.3.14 PIC 3.2H

Problem	Description
Fast data acquisition is needed for the I3200, at a minimum time-slice of 20 microseconds per 32x sample.	The FPGA and PIC code has been modified to enable this fast mode.

5/7/2008 FPGA 5.3.14 PIC 3.2I

Problem	Description
The fiber-optic triggering for the optical gate is level triggered instead of edge triggered.	This has been change to edge triggered.

5/22/2008 FPGA 5.3.14 PIC 3.2J

Problem	Description
Code for the Rev0 I3200 has been removed.	The I3200 Rev0 version has been established as a compile-time switch. Users of Rev0 should use 3.2K.

5/22/2008 FPGA 5.3.5 PIC 3.2K

Problem	Description
Code for the Rev0 I3200 has been established with a compile time switch.	This version is equivalent to Rev 3.2J above, but is for exclusive use with the Revision 0 board.

6/2/2008 5.3.14 PIC 3.2L

Problem	Description
When the programmable gain amplifier (PGA) is not set to 1, the integration time is not properly set to the displayed value.	This was due to a bug with the ConfigureGateInternalRange command, which was not properly accounting for the PGA value. Fixed.



Pyramid Technical Consultants, Inc.
1050 Waltham Street, Lexington, MA 02421
TEL: (781) 402-1700 ♦ FAX: (781) 402-1750 ♦ WEBSITE: <http://www.ptcusa.com>

I400 Version Notes